CLAIMS:

1. A microcontroller, wherein the microcontroller (10) has at least one status bit (12) by means of which a writing and/or reading of N-bit address words by at least one standard instruction of the microcontroller (10) can be forced, wherein the address length N of the N-bit address word is greater than the address length of a standard set of instructions or of equivalents of other sets of instructions of the microcontroller.

2. A microcontroller as claimed in claim 1, characterized in that the address length N of the N-bit address word is greater than 16.

3. A microcontroller as claimed in claim 2, characterized in that the address length N of the N-bit address word has the value 20, 24 or 32.

4. A microcontroller as claimed in any of the preceding claims, characterized in that the at least one standard instruction is an LCALL, ACALL or RET instruction or the like.

5. A microcontroller as claimed in any of the preceding claims, characterized in that the at least one status bit (12) can be set and/or deleted by means of at least one computer-readable storage medium (14).

6. A microcontroller as claimed in any of the preceding claims, characterized in that the at least one status bit (12) is part of at least one Special Function Register (16).

7. A microcontroller as claimed in any of the preceding claims, characterized in that the at least one status bit (12) is implemented in the hardware of the microcontroller (10).

8. A microcontroller as claimed in any of the preceding claims, characterized by a design for use in a smartcard.

9. An addressing method, characterized in that at least one status bit (12) of a microcontroller (10) is set and as a result a writing and/or reading of N-bit address words by means of at least one standard instruction of the microcontroller (10) is forced.

10. A method as claimed in claim 9, characterized in that the at least one standard instruction is an LCALL, ACALL or RET instruction or the like.

11. A method as claimed in either of claims 9 and 10, characterized in that the at least one status bit (12) is set and/or deleted by means of at least one computer-readable storage medium (14).

12. A method as claimed in any of claims 9 to 11, characterized in that the at least one status bit (12) is part of at least one Special Function Register (16).

13. A method as claimed in any of claims 9 to 12, characterized in that the at least one status bit (12) is implemented in the hardware of the microcontroller (10).